

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	US-4724340-\$.DID. OR US-5390333-\$.DID. OR US-6243779-\$.DID.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 13:10
S2	1	"7212524".pn.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 13:25
S3	26	(time near5 switch\$3) same (disable near5 (port or fanout))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:39
S4	136	370/370.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 16:30
S5	14	((TDM or (time division multiplex\$3)) near3 switch\$3) same ((disable near5 (port or block or group)) or (power near3 (low\$3 or sav\$3)))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 08:53
S6	6	S4 and ((disable near5 (port or block or group)) or (power near3 (low\$3 or sav\$3)))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 15:46
S7	4228	370/351,357,360,369,370,375, 386,492,498.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 15:46
S8	307	S7 and ((disable near5 (port or block or group or fanout)) or (power near3 (low\$3 or sav\$3)))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 16:49
S9	9	S7 and ((disable near5 (port or block or group or fanout)) and (power near3 (low\$3 or sav\$3 or reduc\$4)))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/24 16:08
S10	2	S8 and (timeslot near3 count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/24 16:52
S11	15	S7 and (timeslot near3 count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/24 16:53
S12	4	((TDM or (time division multiplex\$3)) near3 switch\$3) same (grain)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:13
S13	613	370/386.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:13

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S14	19	S13 and (time same (disable or sleep))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:18
S15	1	"3956593" .pn.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:18
S16	40	("3715505" "3740480" "3796835").PN. OR ("3956593").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:19
S17	12484	((time or timeslot or tdm or time division multiplex\$3) near5 switch\$3) same port	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:47
S18	168	((time or timeslot or tdm or time division multiplex\$3) near5 switch\$3) same (port near3 group)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/26 09:55
S19	31440	switch same disable	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/27 11:57
S20	283	switch same (disable near3 port)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/27 12:19
S21	1417	(switch or bridge or router or gateway) near5 (disabl\$3 near3 (port or output))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 11:15
S22	1453	(switch or bridge or router or gateway or matrix or crossswitch or crosspoint) near5 (disabl\$3 near3 (port or output))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/27 12:31
S23	4237	370/351,357,360,369,370,375, 386,492,498.ccls.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 11:16
S24	95	S23 and ((disabl\$3 or block\$3) near3 (port or output)).bsum.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 12:50
S25	2728	((disabl\$3 or block\$3) near3 (port or output)) same (sleep\$3 or ((sav\$3 or low\$3 or reduc\$3) near3 (power or energy)))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 13:13
S26	2998	((disabl\$3 or block\$3 or sleep\$3 or standby) near3 (port or output)) same ((sav\$3 or low\$3 or reduc\$3) near3 (power or energy))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 13:12
S27	395	((disabl\$3 or block\$3) near3 (port or output)) same (sleep\$3 or ((sav\$3 or low\$3 or reduc\$3) near3 (power or energy))).bsum.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/03 13:13

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S28	30	((disabl\$3 or block\$3) near3 (port or output)) same (sleep\$3 or ((sav\$3 or low\$3 or reduc\$3) near3 (power or energy))) and (flip flop).bsum.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 14:47
S29	150	((disabl\$3 or block\$3) near3 (port or output)) same fan\$1out	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 14:51
S30	57	((select\$3 or choos\$3) near3 (output or port)) with fan\$3out	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:02
S31	0	((select\$3 or choos\$3) near3 (path)) with (fan\$3out near3 tree)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:03
S32	28	((select\$3 or choos\$3) near3 (path)) with (fan\$3out)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:06
S33	828	((select\$3 or choos\$3) near3 (path)) with (tree)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:06
S34	1671	((select\$3 or choos\$3 or deci\$4 or determin\$5) near3 (path)) with (tree)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:13
S35	6	(((select\$3 or choos\$3 or deci\$4 or determin\$5) near3 (path)) with (tree)) same fan\$4out	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:08
S36	1718	((select\$3 or choos\$3 or deci\$4 or determin\$5) near3 (path)) with (fan\$4out or tree)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:19
S37	7	S34 same ((output or egress) near3 (port))	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:20
S38	62	fanout tree	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:35
S39	2	fan\$1out tree same (multi\$1cast\$3 or point\$1to\$1multipoint)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:37
S40	0	fan\$1out tree same (port or output or input) same scalab\$5	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:38
S41	47	fan\$1out tree same (port or output or input)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 15:54
S42	97	((enabl\$3 or disabl\$3) near3 (child or leaf or sub\$1tree)) with tree	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 16:18

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S43	84	((propagat\$3 or send\$3) near3 (child or leaf or slave)) near5 (parent or root or master)) with (tree)	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/08/05 16:19
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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = MCCROSKEY

First Name = CARL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09022679	6741552	150	02/12/1998	FAULT-TOLERANT, HIGHLY-SCALABLE CELL SWITCHING ARCHITECTURE	MCCROSKEY, CARL
11107582	Not Issued	30	04/14/2005	Multiple tenancy cut thru	MCCROSKEY, CARL
60501861	Not Issued	159	09/11/2003	Opportunistic, low latency extensions to request-grant switch fabric protocols	MCCROSKEY, CARL
60562387	Not Issued	159	04/14/2004	Multiple tenancy cut thru	MCCROSKEY, CARL
60778057	Not Issued	159	03/01/2006	Synchronization primitives for fine-grained parallelism	MCCROSKEY, CARL
09847660	6820159	150	05/03/2001	BUS INTERFACE FOR TRANSFER OF SONET/SDH DATA	MCCROSKEY, CARL D.
10080485	Not Issued	161	02/21/2002	Bus interface for transfer of multiple SONET/SDH rates over a serial backplane	MCCROSKEY, CARL D.
11522381	Not Issued	30	09/18/2006	Bus interface for transfer of multiple SONET/SDH rates over a serial backplane	MCCROSKEY, CARL D.
60202177	Not Issued	159	05/05/2000	Bus interface for transfer of Sonet/SDH data	MCCROSKEY, CARL D.
09766602	6876650	150	01/23/2001	BIPARTITE GRAPHICAL CONNECTION SCHEDULING IN TIME AND SPACE SWITCH FABRICS	MCCROSKEY, CARL DIETZ
09845171	6870831	150	05/01/2001	FLEXIBLE, SELF-ALIGNING TIME AND SPACE SWITCH FABRICS	MCCROSKEY, CARL DIETZ
10354046	Not Issued	168	01/30/2003	Egress selection switch architecture with power management	MCCROSKEY, CARL DIETZ
10765945	Not Issued	30	01/29/2004	Egress selection switch architecture with power management	MCCROSKEY, CARL DIETZ
10812958	Not Issued	30	03/31/2004	Memory egress self selection architecture	MCCROSKEY, CARL DIETZ
10937469	Not Issued	30	09/10/2004	System and method for opportunistic request-grant switching	MCCROSKEY, CARL DIETZ
60201753	Not Issued	159	05/04/2000	Flexible, self-aligning time and space switch fabrics	MCCROSKEY, CARL DIETZ

60201757	Not Issued	159	05/04/2000	Bipartite graphical connection scheduling in time and space switch fabrics	MCCROSKY, CARL DIETZ
60458375	Not Issued	159	03/31/2003	Memory egress self selection architecture, a time-division multiplexed memory switch	MCCROSKY, CARL DIETZ
60490531	Not Issued	159	07/29/2003	Egress selection switch architecture with power management for a time division multiplexed memory switch	MCCROSKY, CARL DIETZ

Inventor Search Completed: No Records to Display.

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<input type="text" value="MCCROSKY"/>	<input type="text" value="CARL"/>

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PALM INTRANET

 Day : Sunday
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Inventor Name Search Result

Your Search was:

Last Name = HUGHES

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08513825	5626234	150	08/29/1995	SIFTING SCREEN	HUGHES, ANDREW
08545655	5667661	150	10/31/1995	WIRE PLATING	HUGHES, ANDREW
08781183	5819952	150	01/09/1997	SIFTING SCREEN	HUGHES, ANDREW
09367379	6675975	150	11/01/1999	FILTERING SCREEN AND SUPPORT FRAME THEREFOR	HUGHES, ANDREW
10025070	6759000	150	12/19/2001	FILTERING SCREEN AND SUPPORT FRAME THEREFOR	HUGHES, ANDREW
10097900	6800163	150	03/13/2002	METHOD OF CONSTRUCTING A FILTER SCREEN	HUGHES, ANDREW
10097918	Not Issued	161	03/13/2002	Apparatus for repairing or refurbishing a filtering screen	HUGHES, ANDREW
10097920	6692599	150	03/13/2002	FILTERING SCREEN AND SUPPORT FRAME THEREFOR	HUGHES, ANDREW
10098005	6713190	150	03/13/2002	REINFORCEMENT FOR A MOULDED FILTERING SCREEN SUPPORT FRAME	HUGHES, ANDREW
10469358	Not Issued	161	08/25/2003	Method of treatment	HUGHES, ANDREW
10473262	Not Issued	71	08/03/2004	Manufacture of a filtering screen	HUGHES, ANDREW
29077683	Not Issued	168	10/07/1997	HANDLE GRIP FOR HAMMERS AND THE LIKE	HUGHES, ANDREW
29085541	D408703	150	03/25/1998	HANDLE GRIP	HUGHES, ANDREW
29102907	D424903	150	04/05/1999	TOOL HANDLE	HUGHES, ANDREW
10520843	Not Issued	30	06/24/2005	N-methyl amino acids	HUGHES, ANDREW B.
07993030	5506950	150	12/18/1992	MEHTOD AND APPARATUS FOR PRODUCING A THREE DIMENSIONAL REPRESENTATION OF A WIRE HARNESS INTO A TWO DIMENSIONAL REPRENSENTATION	HUGHES, ANDREW C.

11387653	Not Issued	30	03/23/2006	Connectivity controlled wire routing	HUGHES, ANDREW CHARLES
08257176	5388417	150	06/09/1994	BAG FOR THE RECOVERY OF USED FREON FROM AN OPERATIONAL SYSTEM	HUGHES, ANDREW J.
08847756	Not Issued	161	04/23/1997	FISHING LURE AND HOOK D-SNAGGER #2	HUGHES, ANDREW JACK
10524963	Not Issued	30	02/18/2005	N-(3-Methoxy-5-methylpyrazin-2-yl)-2-(4-[1,3,4-oxadiazol-2-yl]phenyl)pyridine-3-sulphonamide as an anticancer agent	HUGHES, ANDREW MARK
10530232	Not Issued	30	04/04/2005	5-HT 1B/1D receptor agonists for the treatment of headache resulting from administering an endothelin receptor antagonist	HUGHES, ANDREW MARK
10530794	Not Issued	41	04/08/2005	Therapeutic treatment	HUGHES, ANDREW MARK
11720001	Not Issued	19	01/01/0001	A combination of N-(3-methoxy-5-methylpyrazin-2-yl)-2-(4-[1,3,4-oxadiazol-2-yl]phenyl)pyridine-3-sulphonamide and an anti-mitotic cytotoxic agent	HUGHES, ANDREW MARK
09766602	6876650	150	01/23/2001	BIPARTITE GRAPHICAL CONNECTION SCHEDULING IN TIME AND SPACE SWITCH FABRICS	HUGHES, ANDREW MILTON
09845171	6870831	150	05/01/2001	FLEXIBLE, SELF-ALIGNING TIME AND SPACE SWITCH FABRICS	HUGHES, ANDREW MILTON
10354046	Not Issued	168	01/30/2003	Egress selection switch architecture with power management	HUGHES, ANDREW MILTON
10765945	Not Issued	30	01/29/2004	Egress selection switch architecture with power management	HUGHES, ANDREW MILTON
60201753	Not Issued	159	05/04/2000	Flexible, self-aligning time and space switch fabrics	HUGHES, ANDREW MILTON
60201757	Not Issued	159	05/04/2000	Bipartite graphical connection scheduling in time and space switch fabrics	HUGHES, ANDREW MILTON
60490531	Not Issued	159	07/29/2003	Egress selection switch architecture with power management for a time division multiplexed memory switch	HUGHES, ANDREW MILTON
07367116	D321817	150	06/16/1989	ADJUSTMENT TOOL FOR AN ORTHOPEDIC BRACE	HUGHES, ANDREW W.

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PALM INTRANET
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Inventor Name Search Result

Your Search was:

Last Name = MOK

First Name = WINSTON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08551530	5640398	150	11/01/1995	STATE MACHINE ARCHITECTURE FOR CONCURRENT PROCESSING OF MULTIPLEXED DATA STREAMS	MOK, WINSTON
09045703	6052073	150	03/23/1998	SERIAL TO PARALLEL CONVERTER ENABLED BY MULTIPLEXED FLIP-FLOP COUNTERS	MOK, WINSTON
09459972	6668297	150	12/14/1999	POS-PHY INTERFACE FOR INTERCONNECTION OF PHYSICAL LAYER DEVICES AND LINK LAYER DEVICES	MOK, WINSTON
09472153	6584521	150	12/27/1999	SCALEABLE BANDWIDTH INTERCONNECT FOR SIMULTANEOUS TRANSFER OF MIXED PLESIOSYNCHRONOUS DIGITAL HIERARCHY (PDH) CLIENTS	MOK, WINSTON
09518670	6150965	150	03/03/2000	Serial to parallel converter enabled by multiplexed flip-flop counters	MOK, WINSTON
09608294	6671758	150	06/30/2000	BUS INTERFACE FOR CELL AND/OR PACKET DATA TRANSFER	MOK, WINSTON
09697119	6744787	150	10/27/2000	ADAPTIVE PHASE SHIFT FILTRATION OF POINTER JUSTIFICATION JITTER IN SYNCHRONOUS- PLESIOSYNCHRONOUS SIGNAL DESYNCHRONIZATION	MOK, WINSTON
09756680	Not Issued	61	01/10/2001	System interface for cell and/or packet transfer at aggregate data rates of up to 10 Gb/s	MOK, WINSTON
09845171	6870831	150	05/01/2001	FLEXIBLE, SELF-ALIGNING TIME AND SPACE SWITCH FABRICS	MOK, WINSTON
10945973	7165003	150	09/22/2004	METHOD AND SYSTEM FOR TESTING ASSEMBLED MOBILE DEVICES	MOK, WINSTON
10945981	7117112	150	09/22/2004	METHOD AND SYSTEM FOR THE INTERACTIVE TESTING OF ASSEMBLED WIRELESS COMMUNICATION DEVICES	MOK, WINSTON
11249409	Not	30	10/14/2005	Radio frequency isolation container	MOK, WINSTON

	Issued					
<u>11427577</u>	7203616	150	06/29/2006	A METHOD AND SYSTEM FOR THE INTERACTIVE TESTING OF ASSEMBLED WIRELESS COMMUNICATION DEVICES	MOK, WINSTON	
<u>11556030</u>	Not Issued	93	11/02/2006	METHOD AND SYSTEM FOR TESTING ASSEMBLED MOBILE DEVICES	MOK, WINSTON	
<u>60201753</u>	Not Issued	159	05/04/2000	Flexible, self-aligning time and space switch fabrics	MOK, WINSTON	
<u>60503898</u>	Not Issued	159	09/22/2003	Efficient strictly non-blocking multicast switch architecture for time division multiplexed traffic	MOK, WINSTON	
<u>60693092</u>	Not Issued	159	06/23/2005	Radio frequency isolation chamber	MOK, WINSTON	
<u>09847660</u>	<u>6820159</u>	150	05/03/2001	BUS INTERFACE FOR TRANSFER OF SONET/SDH DATA	MOK, WINSTON K.	
<u>10080485</u>	Not Issued	161	02/21/2002	Bus interface for transfer of multiple SONET/SDH rates over a serial backplane	MOK, WINSTON K.	
<u>11522381</u>	Not Issued	30	09/18/2006	Bus interface for transfer of multiple SONET/SDH rates over a serial backplane	MOK, WINSTON K.	
<u>60202177</u>	Not Issued	159	05/05/2000	Bus interface for transfer of Sonet/SDH data	MOK, WINSTON K.	
<u>09286211</u>	<u>6603776</u>	150	04/05/1999	SYSTEM FOR EFFICIENT BROADBAND DATA PAYLOAD CONVERSION	MOK, WINSTON K.	
<u>07730536</u>	<u>5343482</u>	150	07/16/1991	METHOD AND APPARATUS FOR DETECTING PULSE DENSITY VIOLATIONS IN T1 TRANSMISSION	MOK, WINSTON K. C.	
<u>10354046</u>	Not Issued	168	01/30/2003	Egress selection switch architecture with power management	MOK, WINSTON KI-CHEONG	
<u>10765945</u>	Not Issued	30	01/29/2004	Egress selection switch architecture with power management	MOK, WINSTON KI-CHEONG	
<u>10812958</u>	Not Issued	30	03/31/2004	Memory egress self selection architecture	MOK, WINSTON KI-CHEONG	
<u>10830031</u>	Not Issued	30	04/23/2004	Method and apparatus for reducing current demand variations in large fan-out trees	MOK, WINSTON KI-CHEONG	
<u>10946178</u>	Not Issued	30	09/22/2004	Efficient strictly non-blocking multicast switch architecture for time division multiplexed traffic	MOK, WINSTON KI-CHEONG	
<u>60458375</u>	Not Issued	159	03/31/2003	Memory egress self selection architecture, a time-division multiplexed memory switch	MOK, WINSTON KI-CHEONG	
<u>60490531</u>	Not Issued	159	07/29/2003	Egress selection switch architecture with power management for a time division multiplexed memory switch	MOK, WINSTON KI-CHEONG	

<u>60831910</u>	Not Issued	159	07/19/2006	Hit-less equipment protection	MOK, WINSTON KI-CHEONG
<u>08989162</u>	<u>6188699</u>	150	12/11/1997	MULTI-CHANNEL ENCODER/DECODER	MOK, WINSTON KI-CHEONG
<u>09057509</u>	<u>6333935</u>	150	04/09/1998	STATE MACHINE ARCHITECTURE WITH MULTIPLEXED RANDOM ACCESS MEMORY	MOK, WINSTON KI-CHEONG
<u>60464855</u>	Not Issued	159	04/24/2003	Method of eliminating current demand variations in large fan-out trees	MOK, WINSTON KI-CHEONG
<u>11297487</u>	Not Issued	30	12/09/2005	Transport frame and optional fixture for battery-powered electronic devices	MOK, WINSTON Y.W.

Inventor Search Completed: No Records to Display.

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 PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ROLHEISER

First Name = NICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10765945	Not Issued	30	01/29/2004	Egress selection switch architecture with power management	ROLHEISER, NICK
60464855	Not Issued	159	04/24/2003	Method of eliminating current demand variations in large fan-out trees	ROLHEISER, NICK
60490531	Not Issued	159	07/29/2003	Egress selection switch architecture with power management for a time division multiplexed memory switch	ROLHEISER, NICK

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